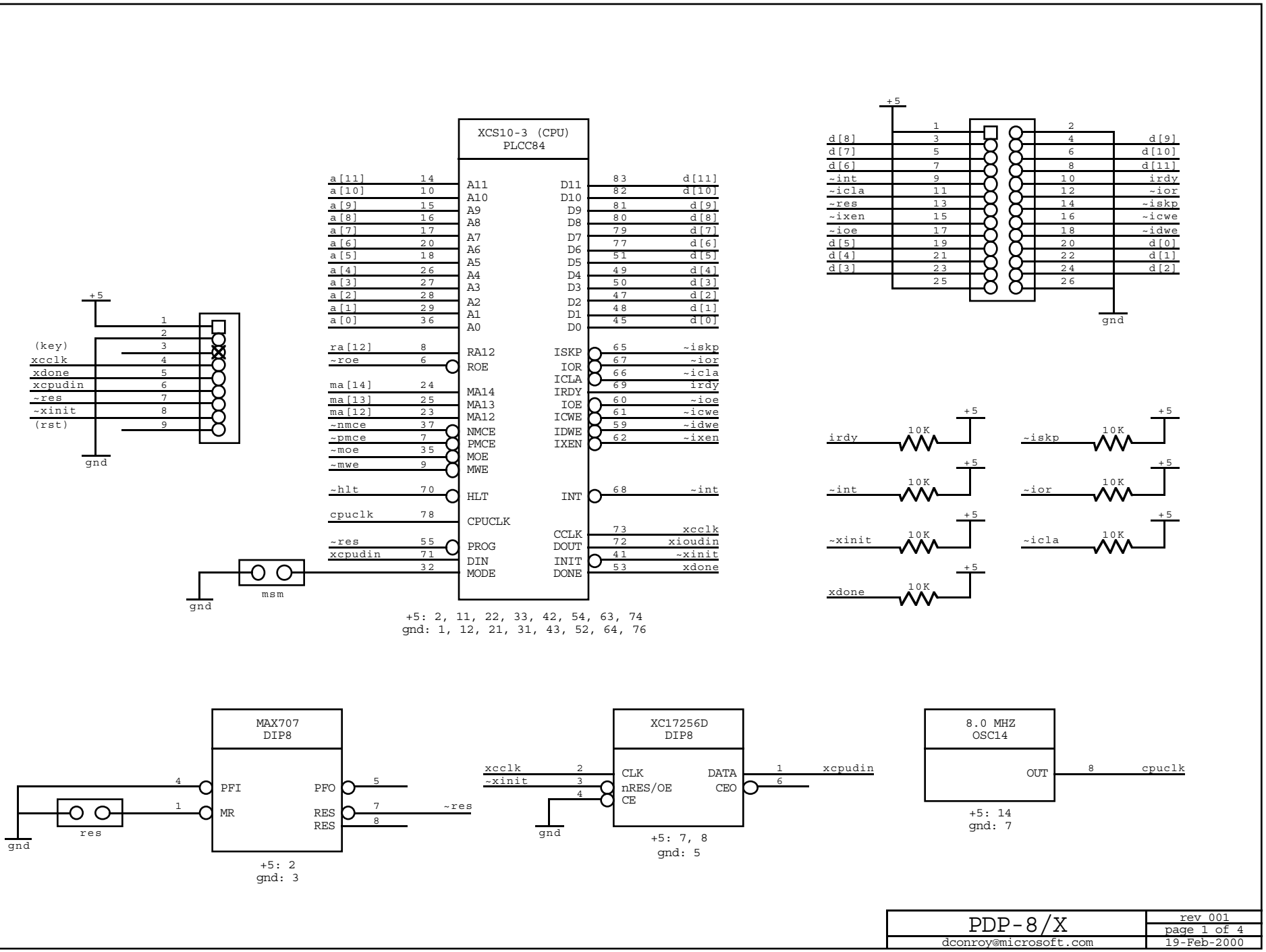
XCS10-3 (CPU)
PLCC84
+5
gnd
msm
(key)
xcclk
xdone
xcpudin
~res
~xinit
(rst)
a[11] 14 A11
a[10] 10 A10
a[9] 15 A9
a[8] 16 A8
a[7] 17 A7
a[6] 20 A6
a[5] 18 A5
a[4] 26 A4
a[3] 27 A3
a[2] 28 A2
a[1] 29 A1
a[0] 36 A0
ra[12] 8 RA12
~roe 6 ROE
ma[14] 24 MA14
ma[13] 25 MA13
ma[12] 23 MA12
~nmce 37 NMCE
~pmce 7 PMCE
~moe 35 MOE
~mwe 9 MWE
~hlt 70 HLT
cpuclk 78 CPUCLK
~res 55 PROG
xcpudin 71 DIN
32 MODE
83 D11 d[11]
82 D10 d[10]
81 D9 d[9]
80 D8 d[8]
79 D7 d[7]
77 D6 d[6]
51 D5 d[5]
49 D4 d[4]
50 D3 d[3]
47 D2 d[2]
48 D1 d[1]
45 D0 d[0]
65 ISKP ~iskp
67 IOR ~ior
66 ICLA ~icla
69 IRDY irdy
60 IOE ~ioe
61 ICWE ~icwe
59 IDWE ~idwe
62 IXEN ~ixen
68 INT ~int
73 CCLK xcclk
72 DOUT xioudin
41 INIT ~xinit
53 DONE xdone
+5: 2, 11, 22, 33, 42, 54, 63, 74
gnd: 1, 12, 21, 31, 43, 52, 64, 76
d[8] d[9]
d[7] d[10]
d[6] d[11]
~int irdy
~icla ~ior
~res ~iskp
~ixen ~icwe
~ioe ~idwe
d[5] d[0]
d[4] d[1]
d[3] d[2]
irdy ~iskp
~int ~ior
~xinit ~icla
xdone
10K
MAX707
DIP8
PFI
MR
PFO
RES
RES
res
+5: 2
gnd: 3
XC17256D
DIP8
xcclk
~xinit
CLK
nRES/OE
CE
DATA
CEO
xcpudin
+5: 7, 8
gnd: 5
8.0 MHZ
OSC14
OUT
cpuclk
+5: 14
gnd: 7
PDP-8/X
dconroy@microsoft.com
rev 001
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19-Feb-2000